

FIG. 1 (PRIOR ART)

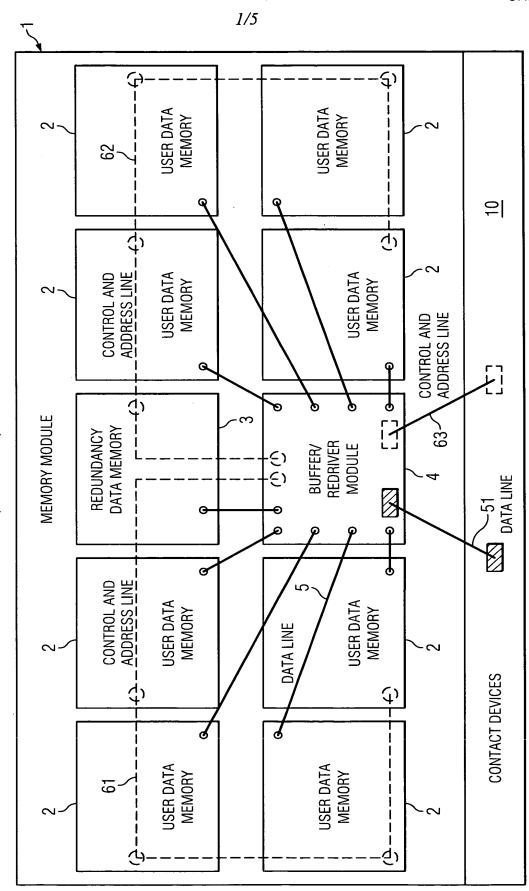




FIG. 2 (PRIOR ART)

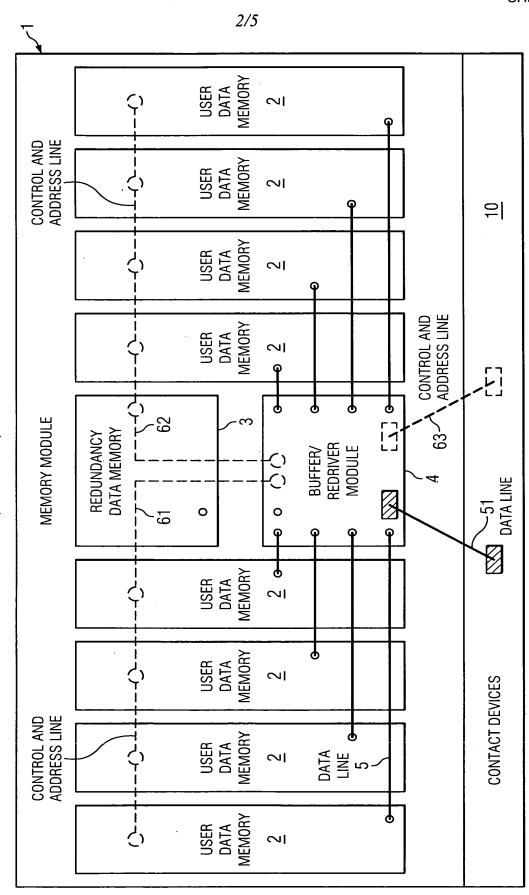




FIG. 3

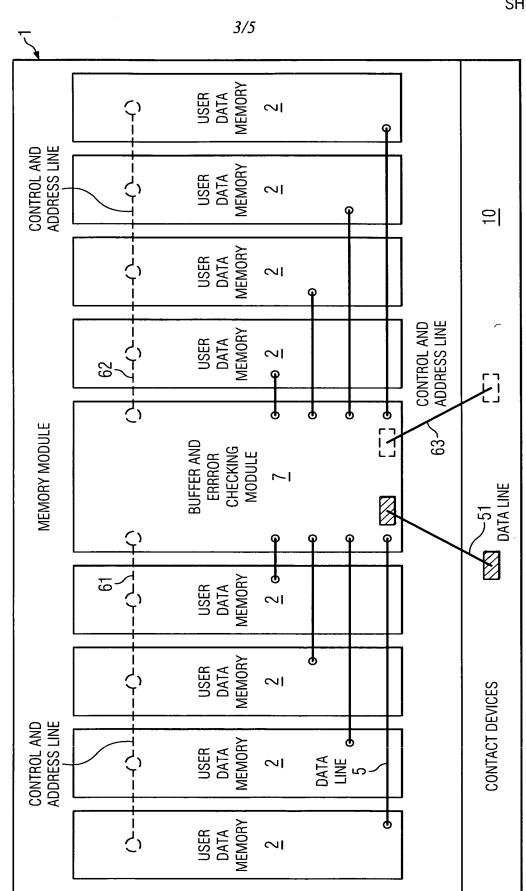
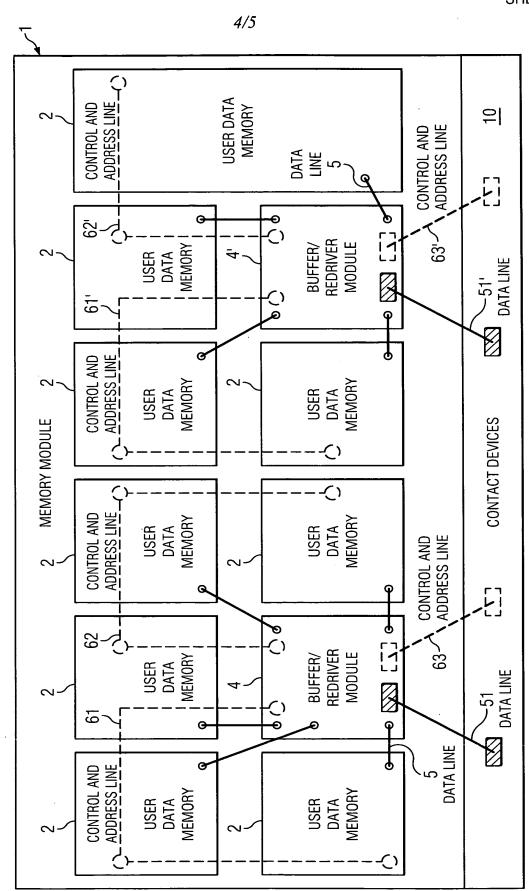




FIG. 4 (PRIOR ART)





5/5 **USER DATA USER DATA** MEMORY MEMORY 위 CONTROL AND ADDRESS LINE ठ BUFFER AND ERROR 63.7 CHECKING MODULE ン51' DATA LINE **USER DATA USER DATA** MEMORY MEMORY MEMORY MODULE CONTACT DEVICES FIG. 5 **USER DATA USER DATA** MEMORY MEMORY CONTROL AND ADDRESS LINE S BUFFER AND ERROR CHECKING MODULE 637 DATA LINE DATA LINE -51 61 C ADDRESS LINE CONTROL AND USER DATA MEMORY **USER DATA** MEMORY